

Serial No. 09/464811



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

Inventor(s): Shao

Case: 1-10-3

Serial No.: 09/464811

Filing Date: 12/17/99

Examiner: Cruz

Group Art Unit: 2827

Title: Integration of Low K Dielectric Material In Semiconductor Circuit Structures

ASSISTANT COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

SIR:

Letter to Official Draftsman

In response to the Notice of Allowance dated April 22, 2003, for the above-identified application, please make of record the attached 12 sheets of **formal** drawings.

Respectfully,

By Ferdinand M. Romano
Ferdinand M. Romano
Reg. No. 32752
407-371-3250

Date: 7/1/03

Date of Deposit
7-2-03
I hereby certify that these ~~Formal Drawings~~
being deposited with the United States Postal
Service "First Class Mail" on the
date indicated above and is addressed to the
Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450.
Mary V. Carter
(Printed name of person mailing paper or fee)
Mary V. Carter
(Signature of person mailing paper or fee)